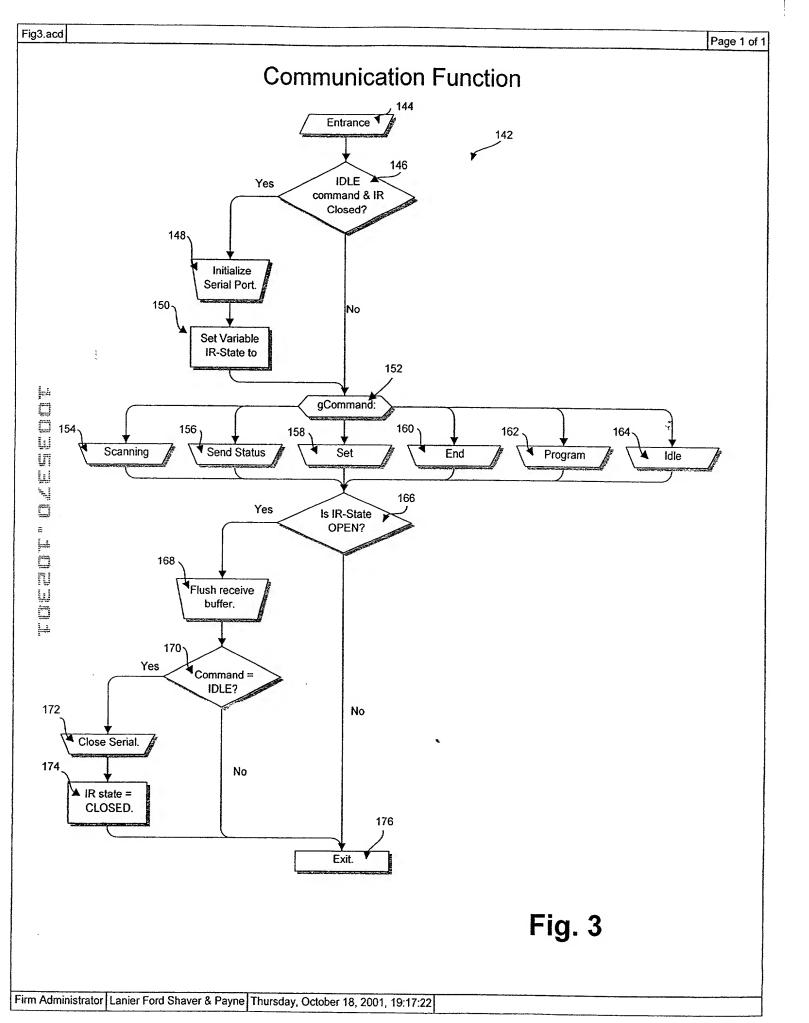


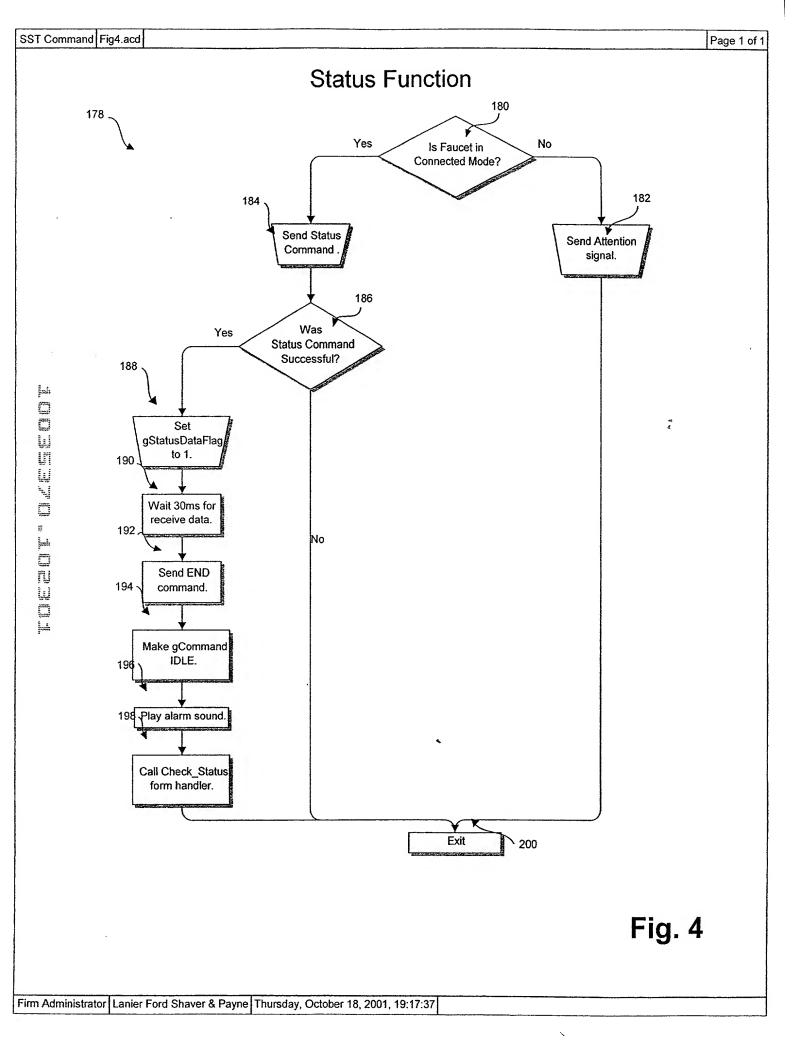
FIG.

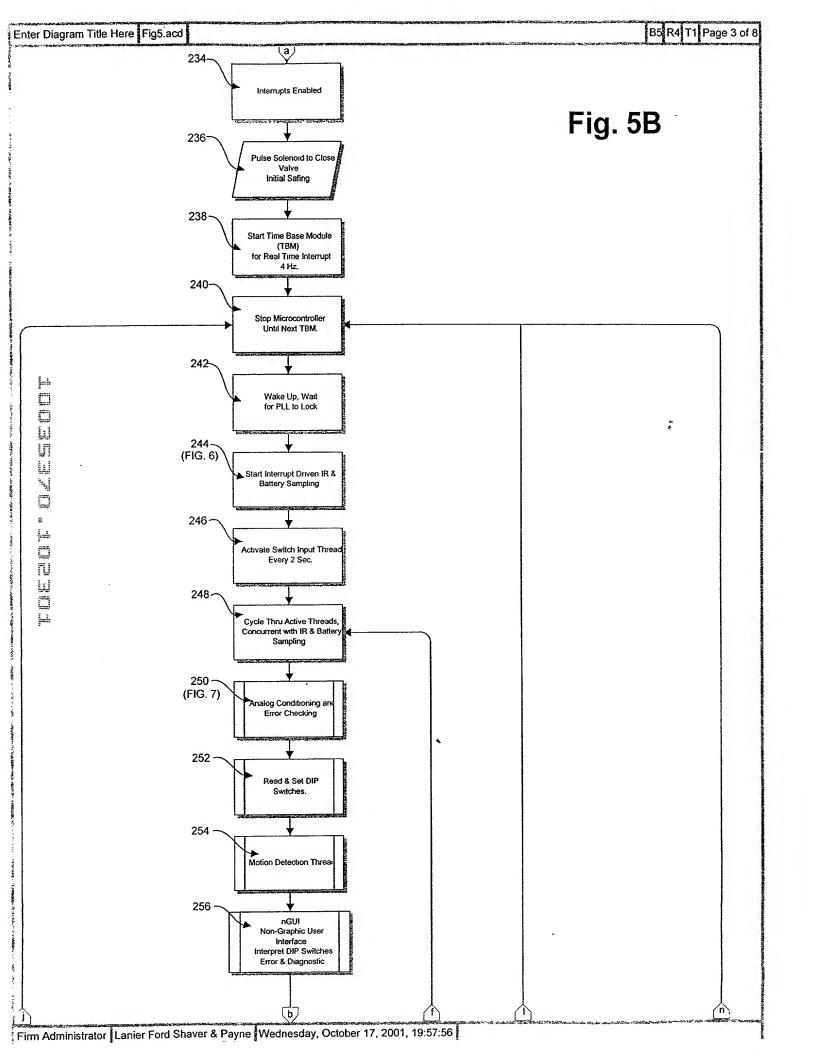
the Palm event handlers.

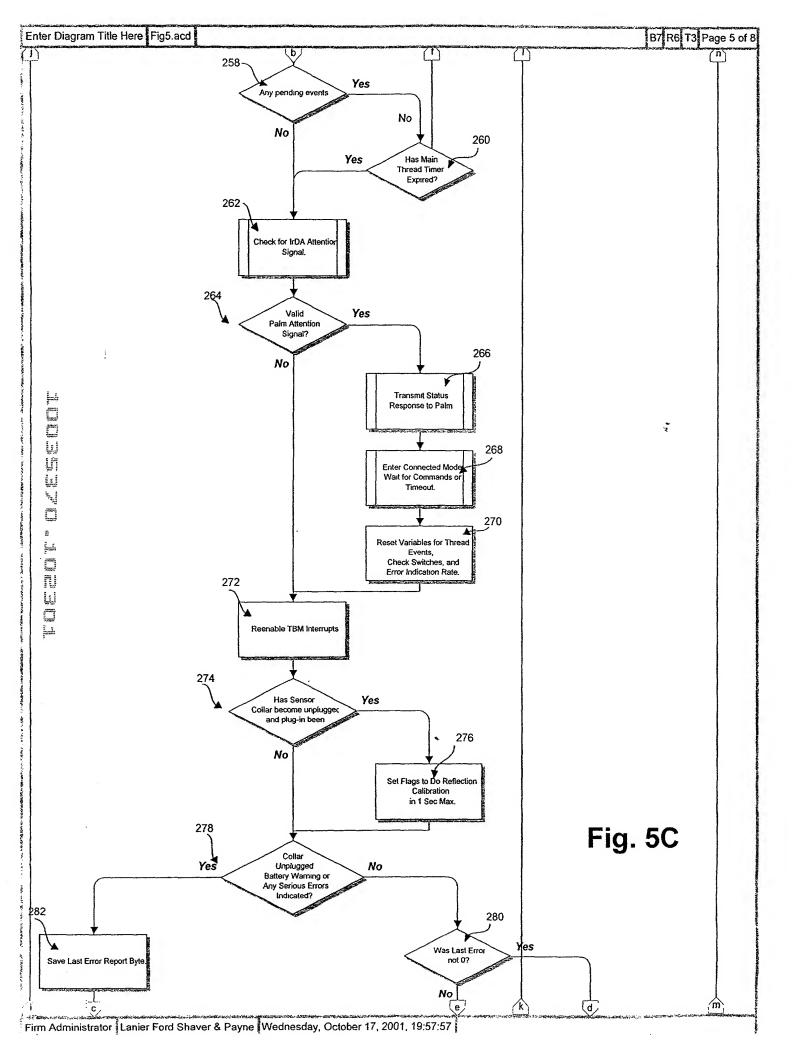
140

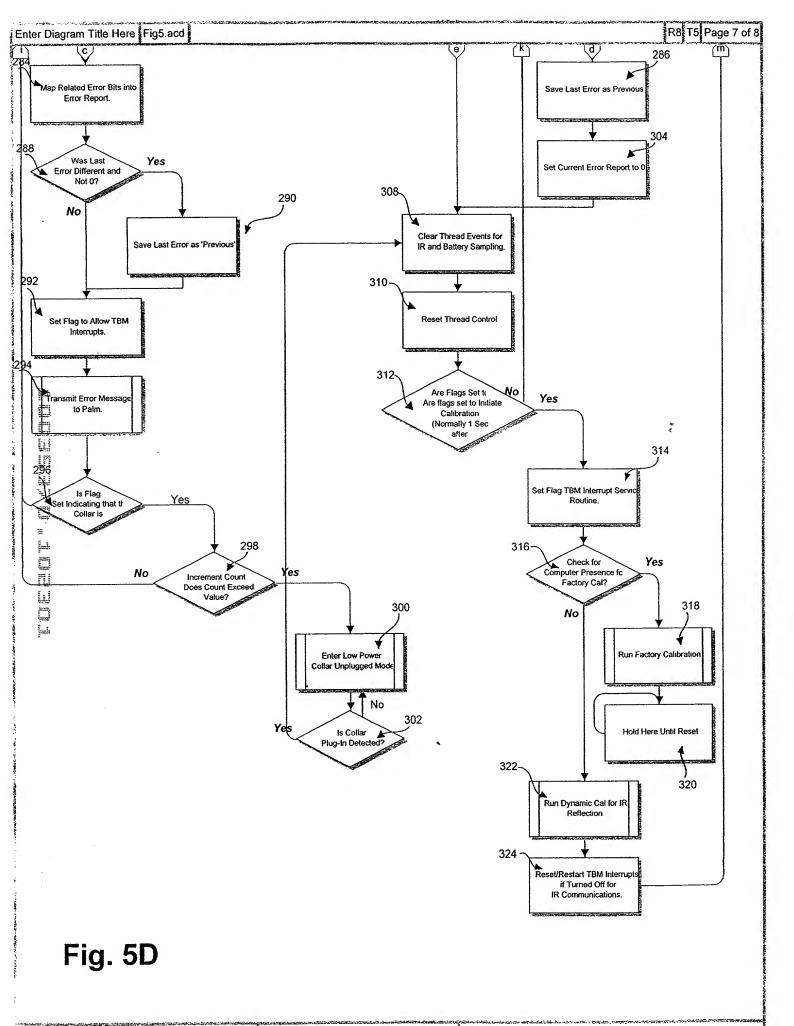
Fig. 2

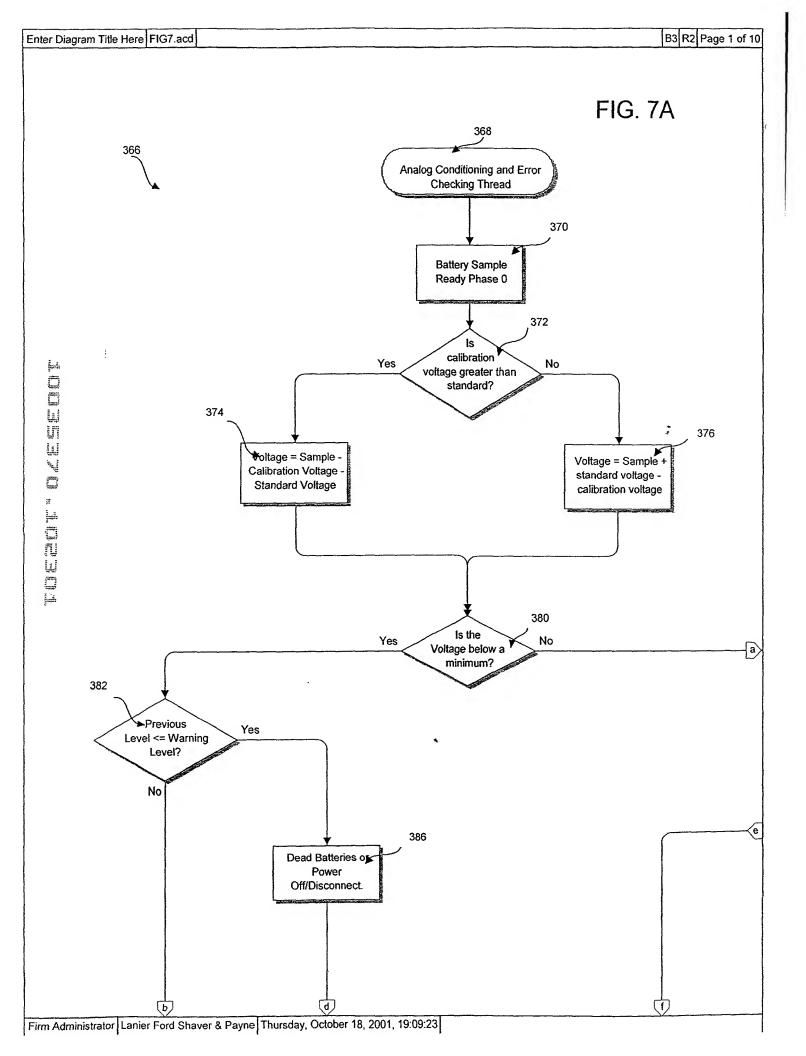


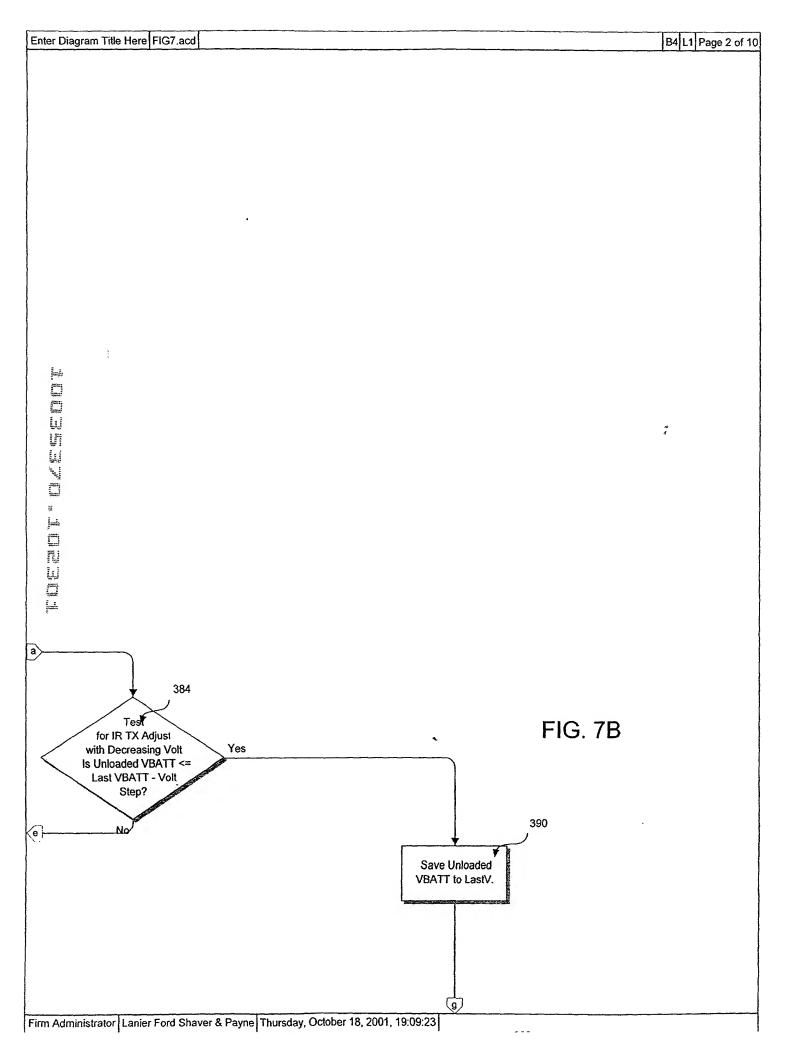


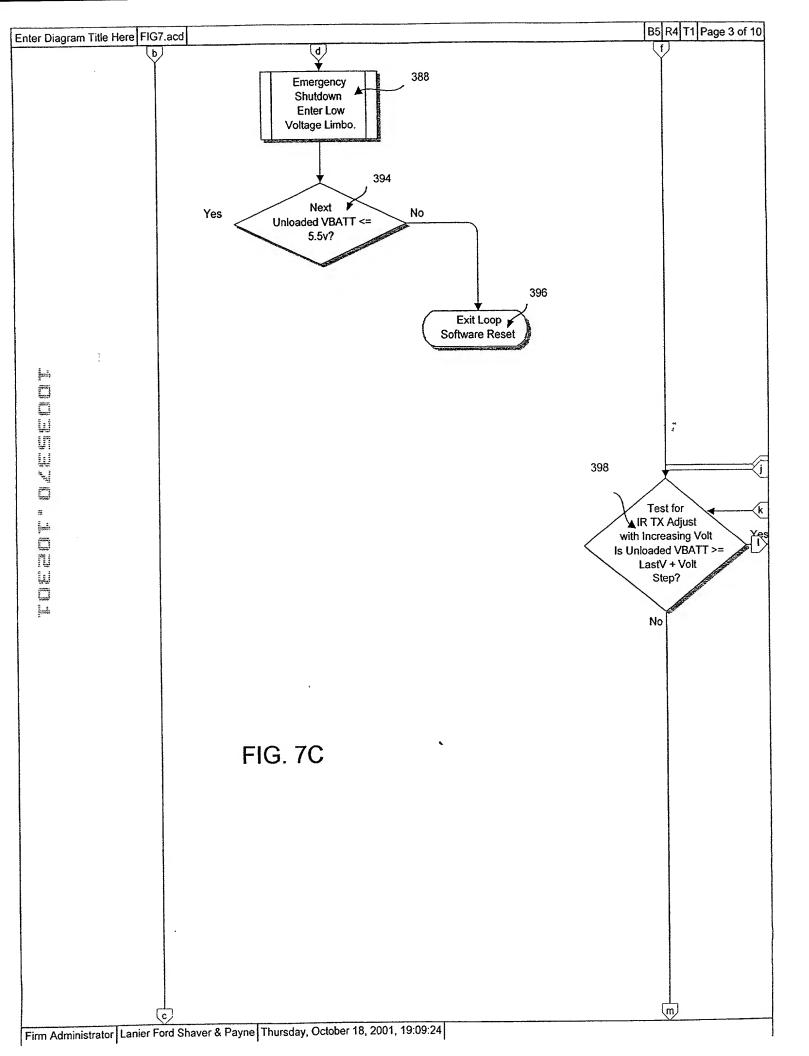


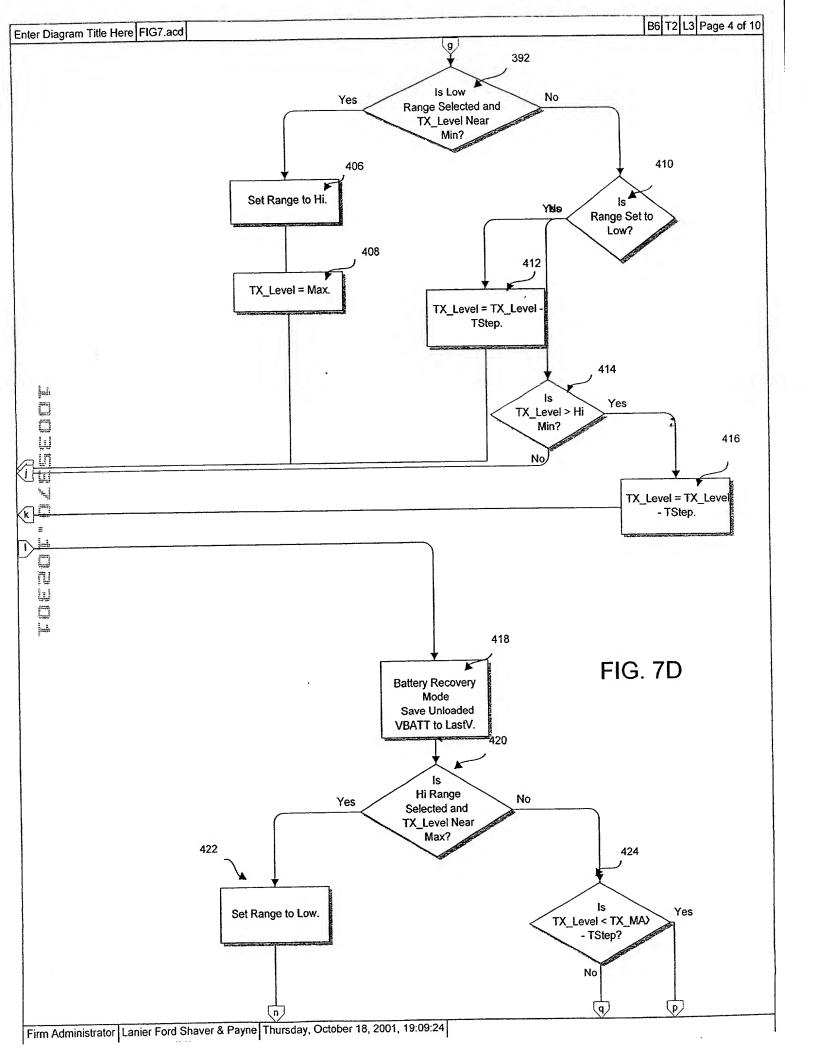


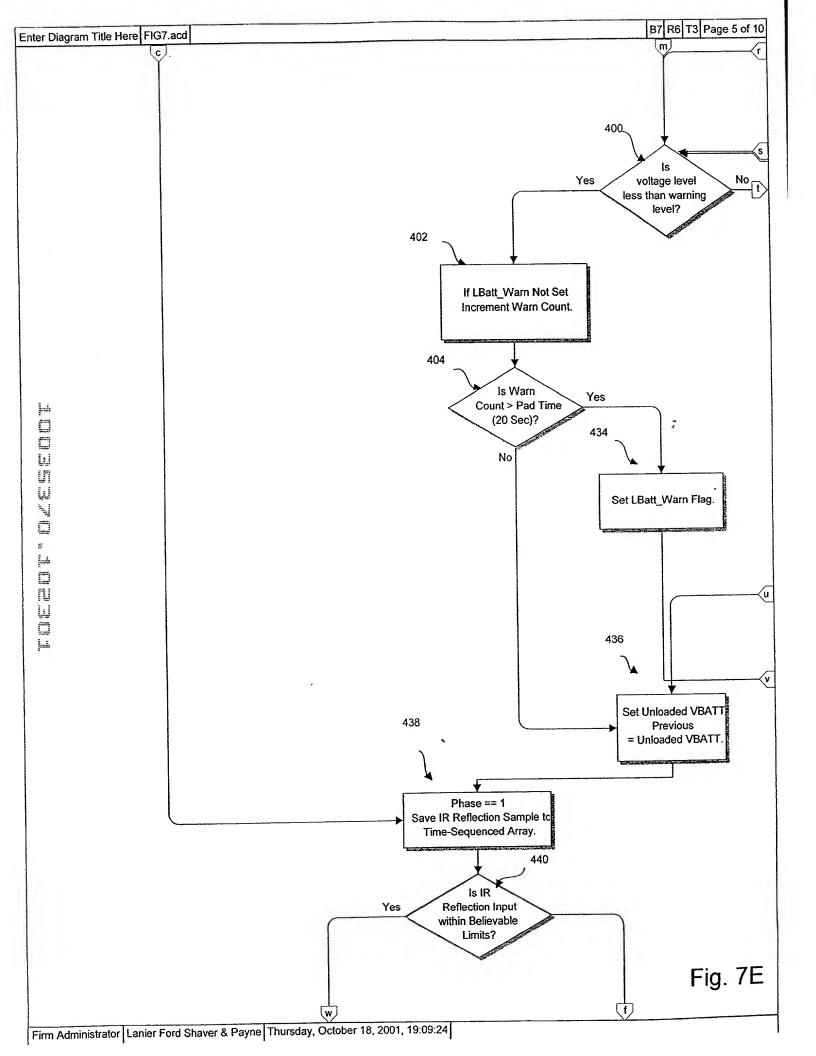


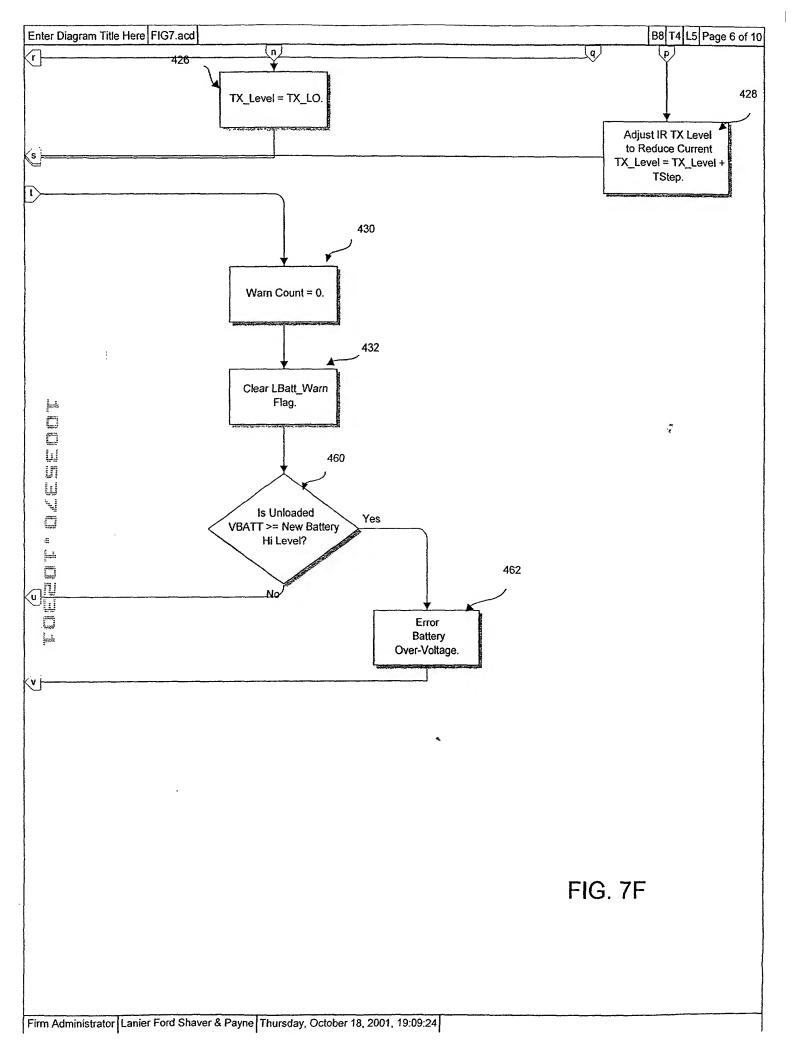


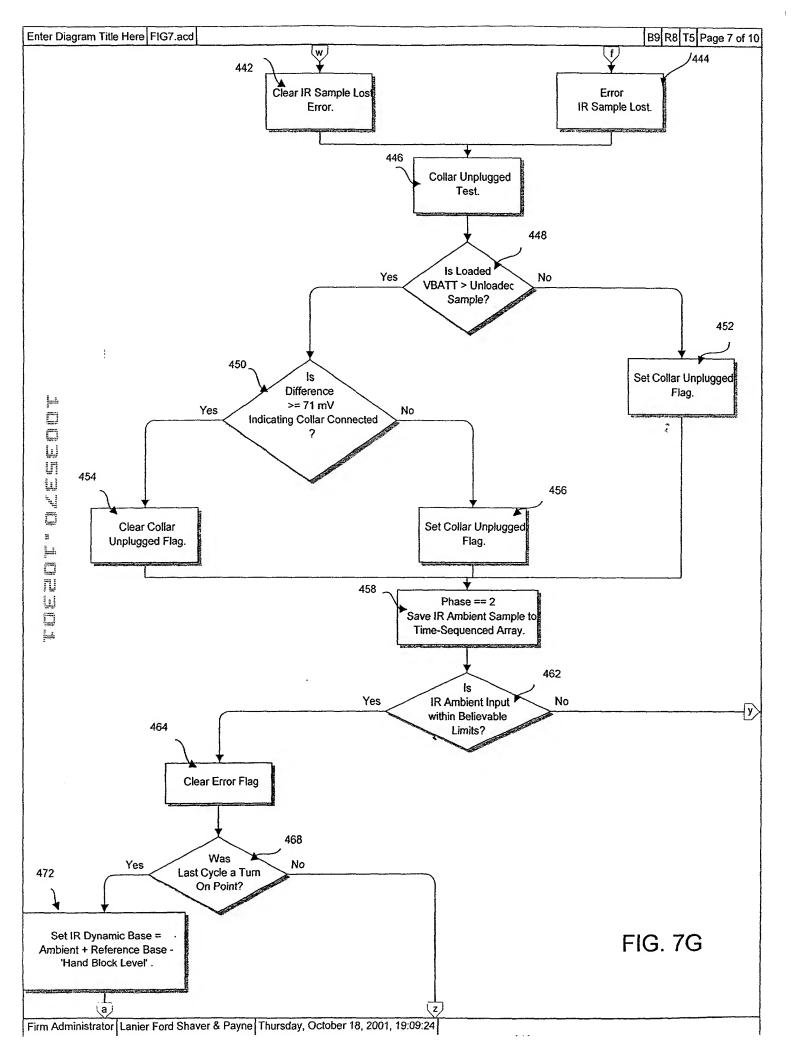


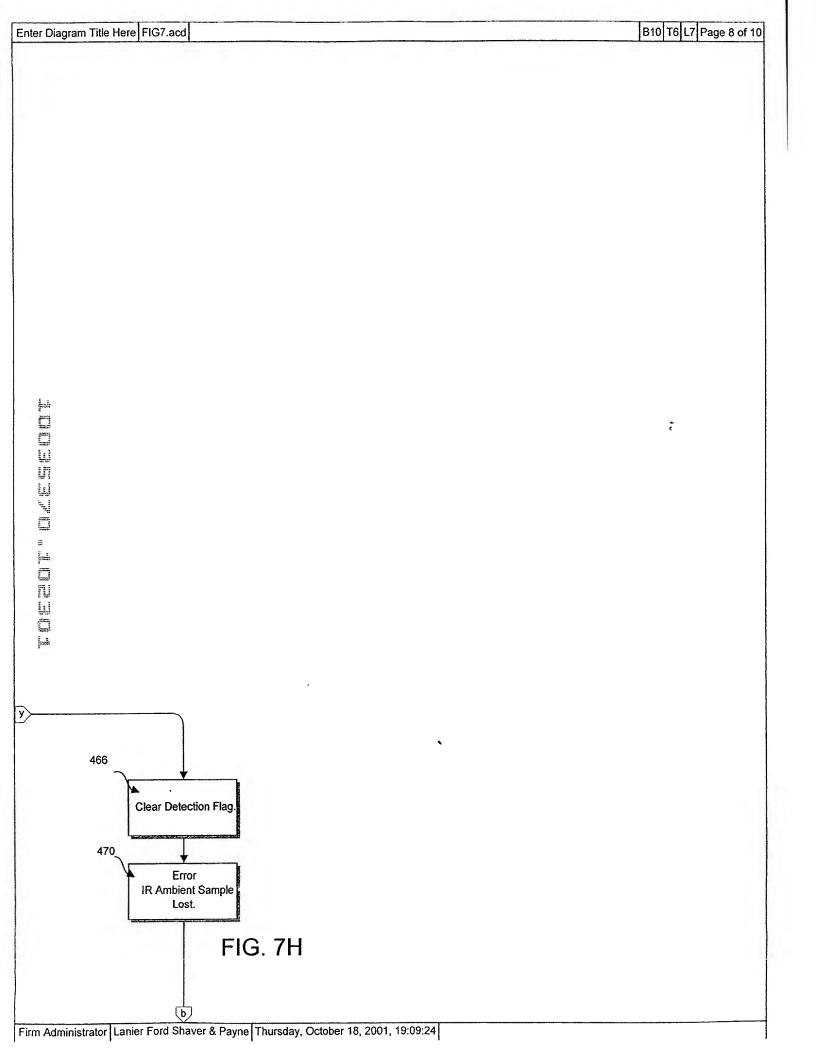


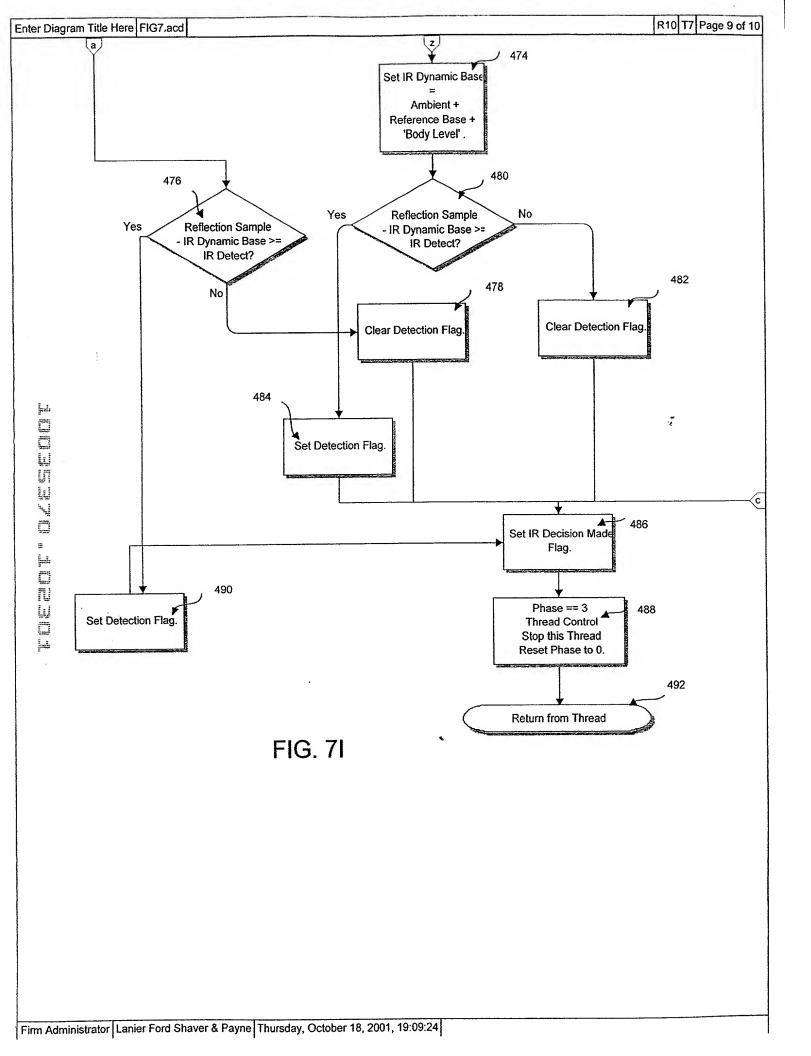


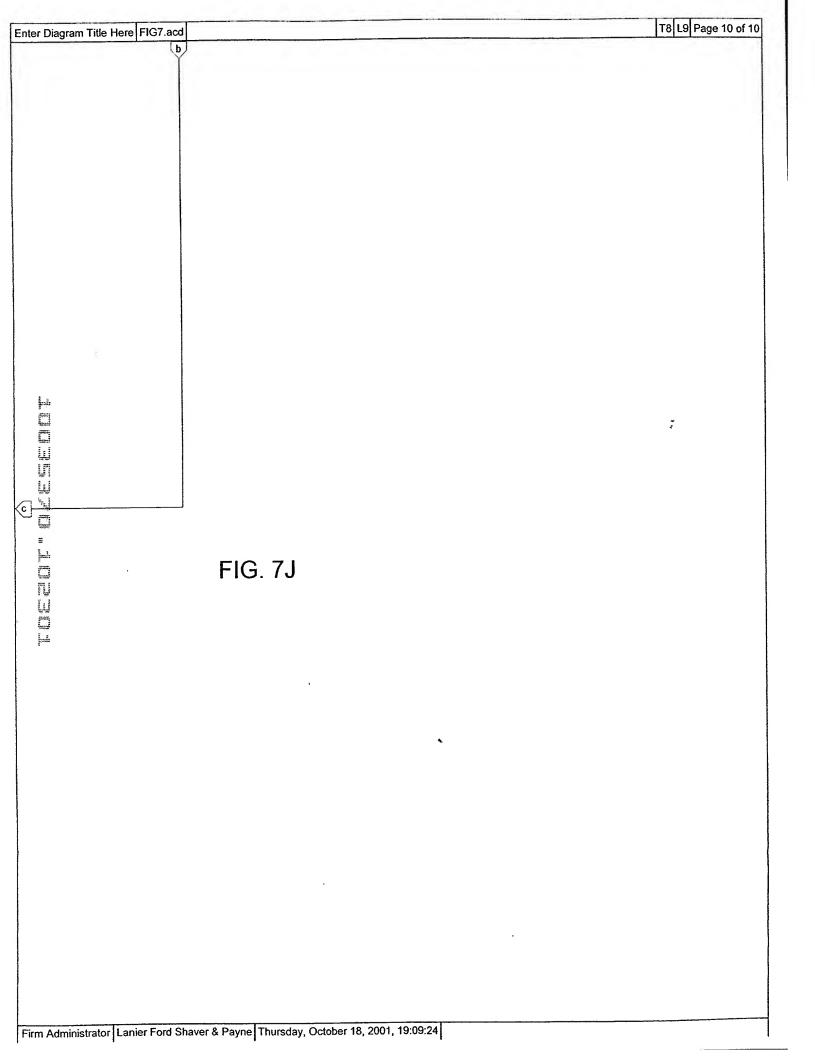


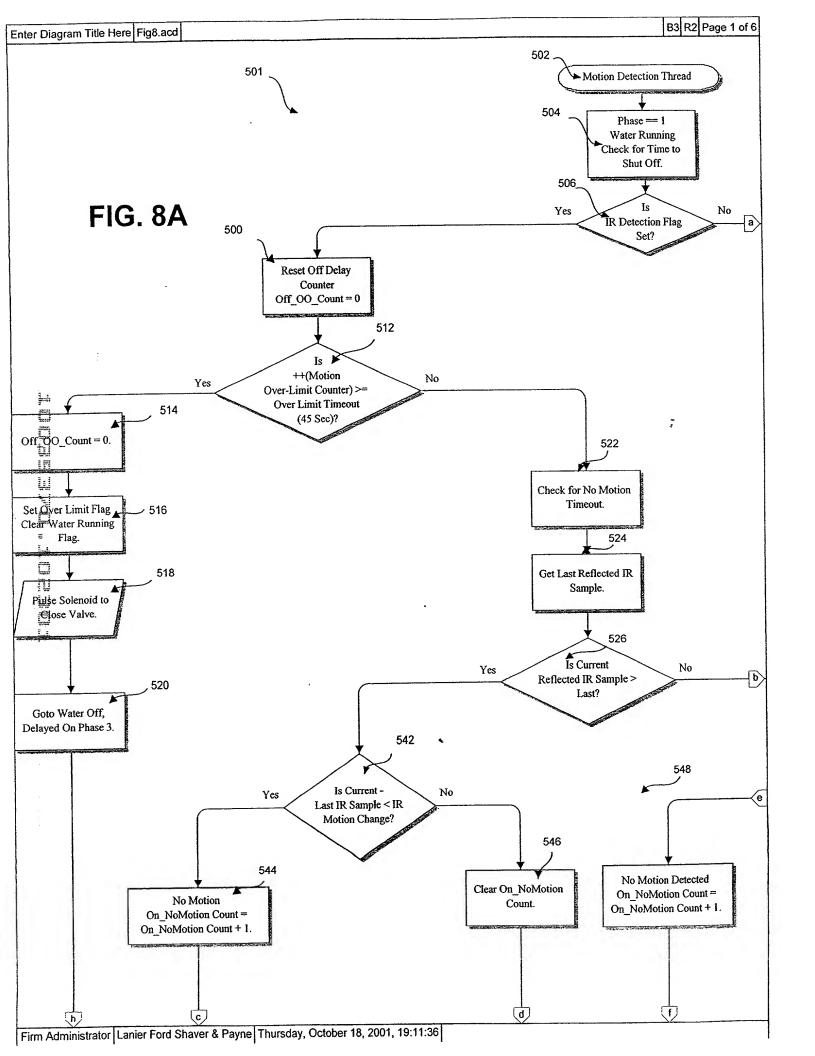


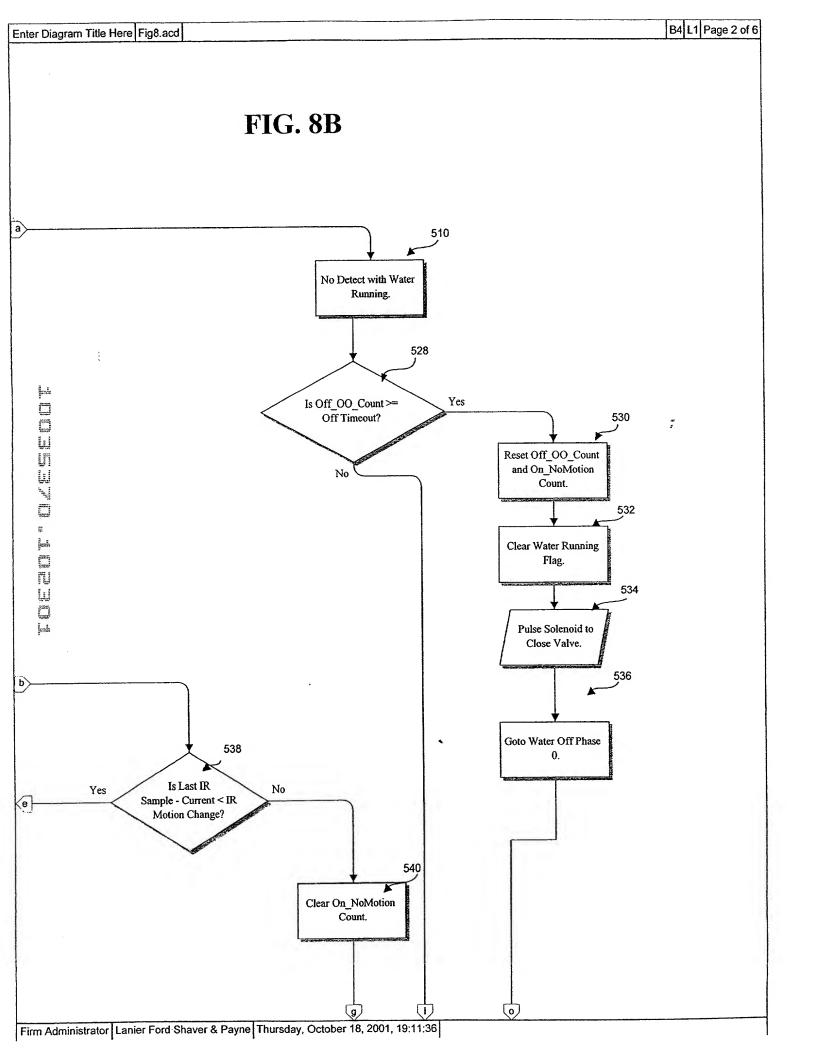


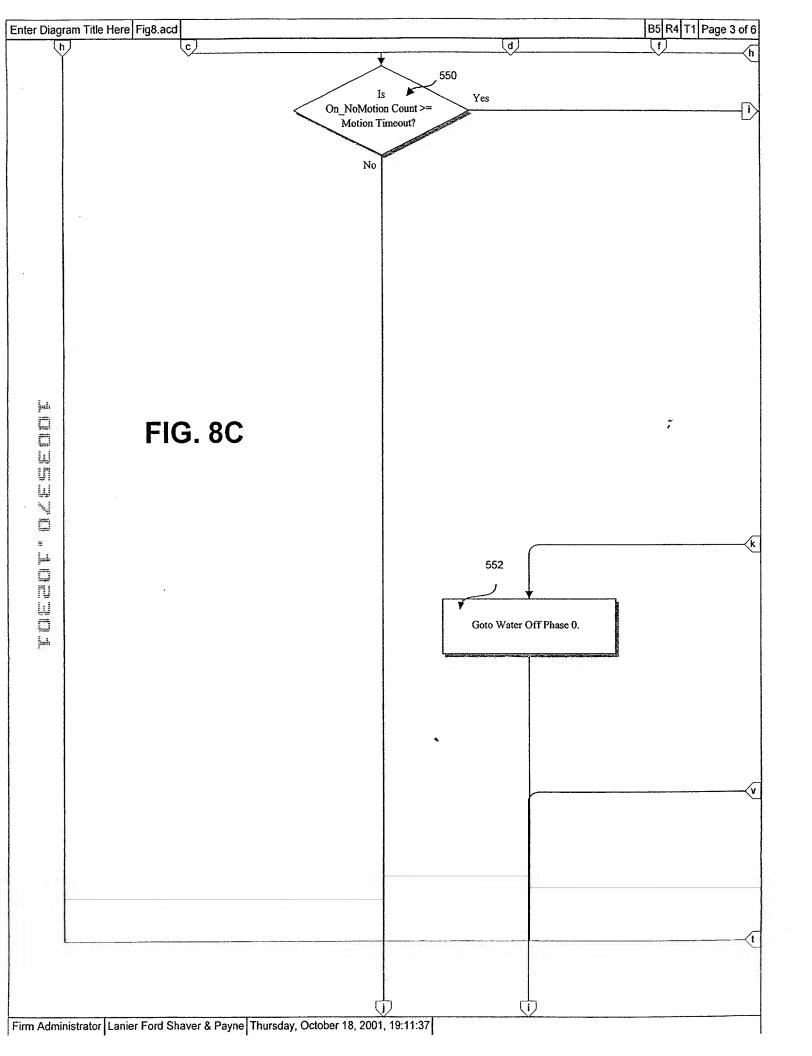


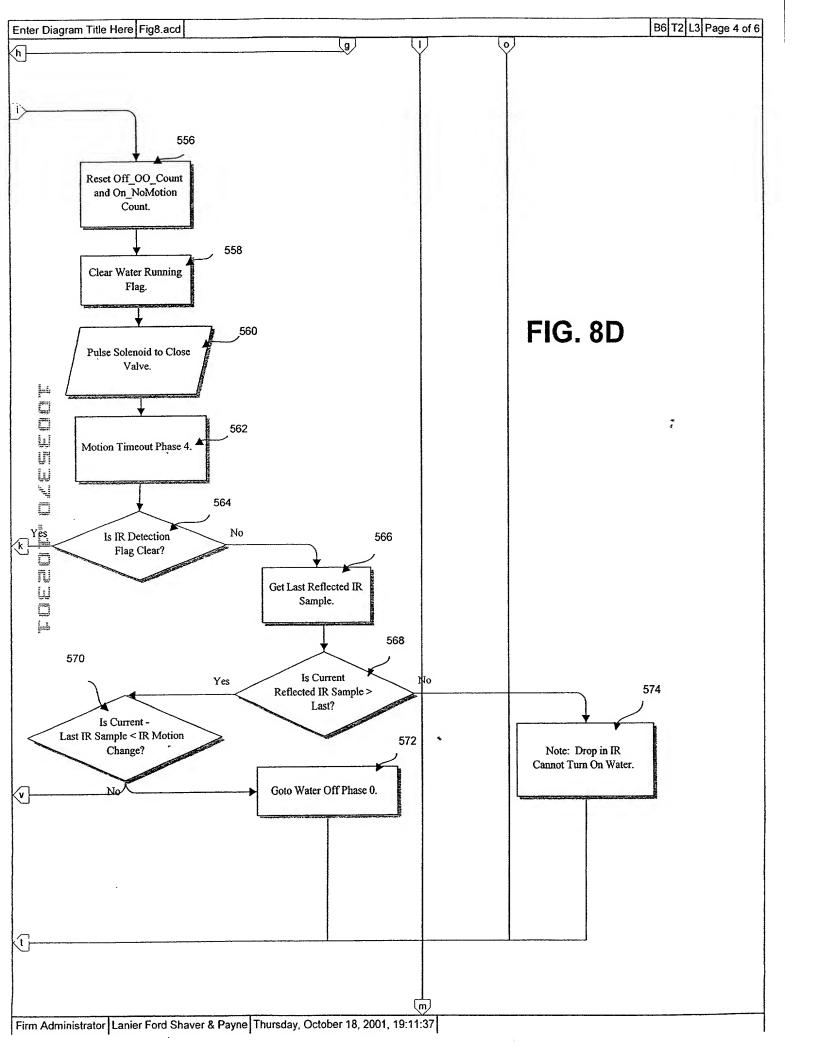




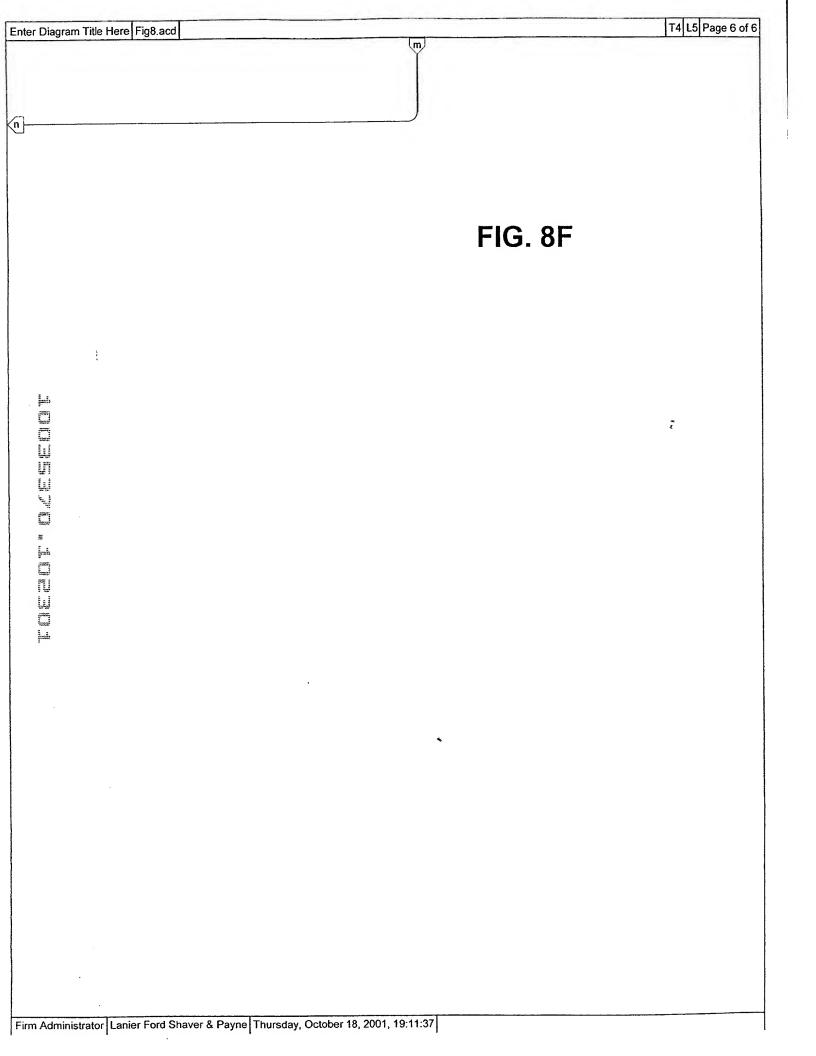


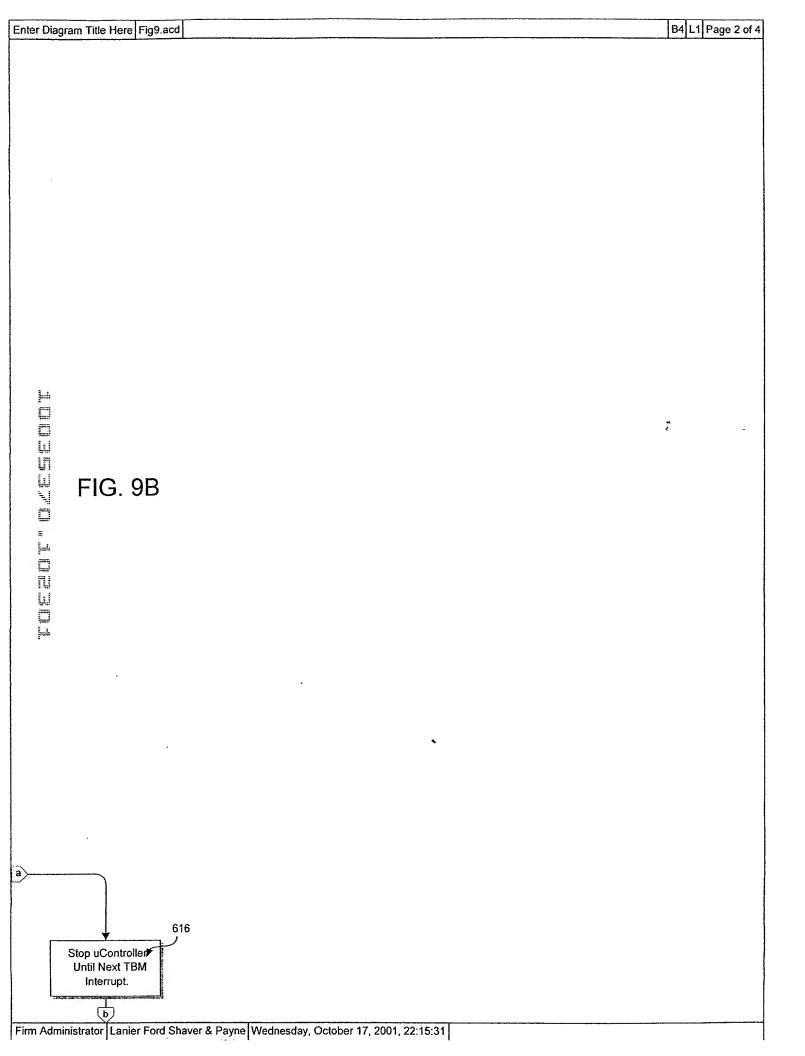


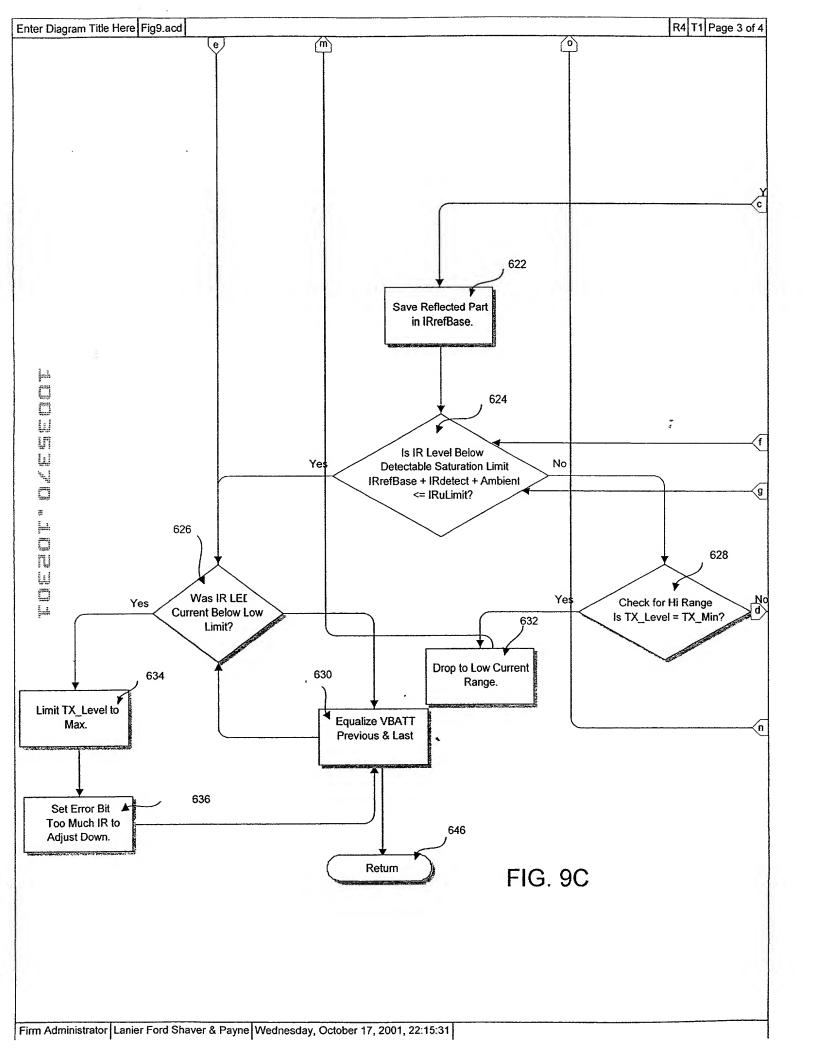


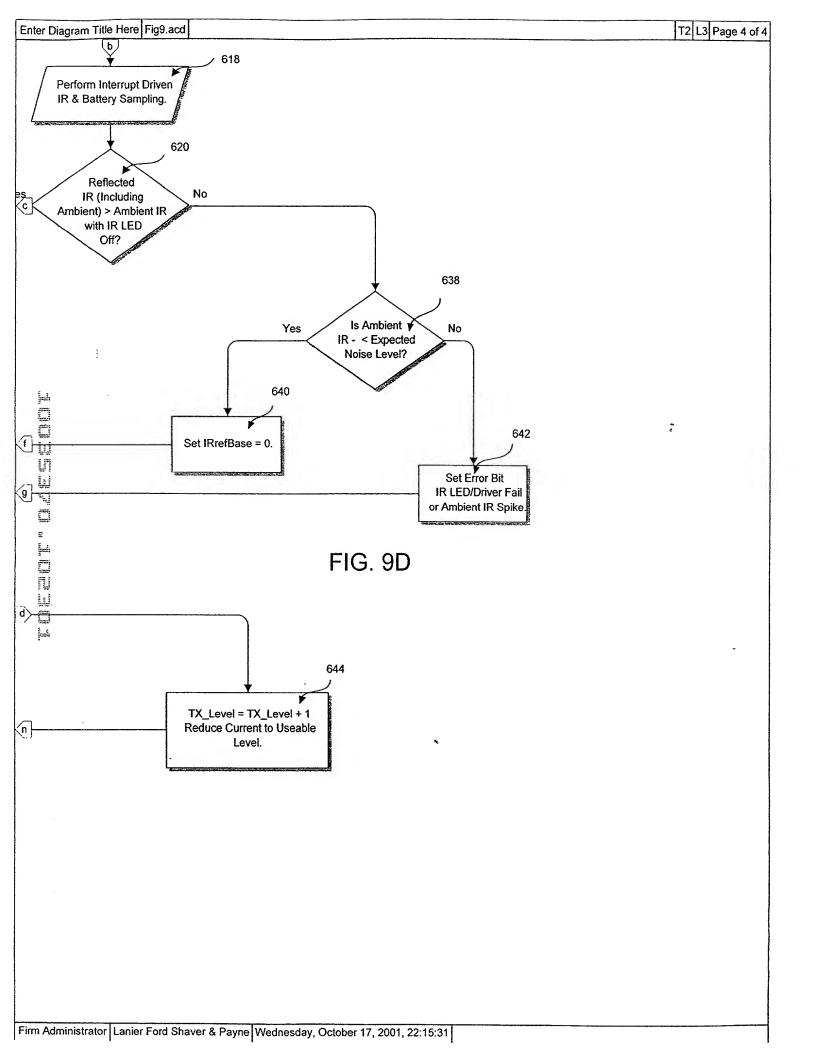












1

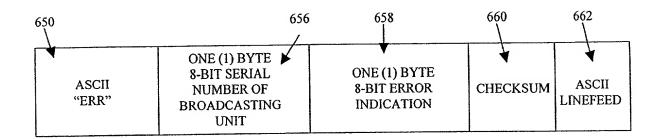


FIG. 10

4

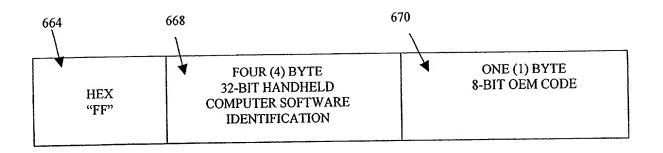
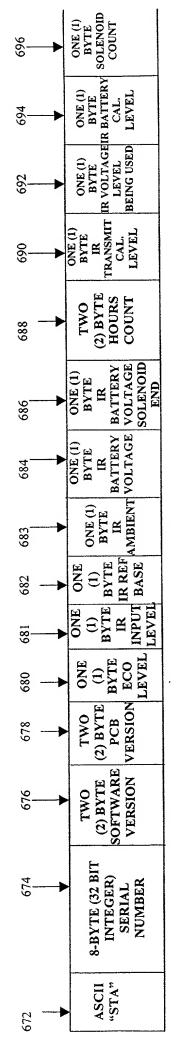


FIG. 11

ĩ



ONE (1) CHECKSUM TERMINATION BYTE MODULO INDICATOR SPARE (CS) LINEFEED (LF)	-	889
CHECKSUM MODULO (CS)	←	989
ONE (1) BYTE SPARE	←	684
ONE (1) C BYTE 1 STATUS S	—	682
ONE (1) BYTE CURRENT ERROR BITS	←	089
ONE (1) BYTE PAST ERROR BITS	←	879
ONE (1) BYTE DELAY IN SECONDS	← —	929
ONE (1) BYTE RANGE OFFSET	-	674
ONE (1) BYTE VIRTUAL DIP SWITCH SETTINGS	←	672
ONE (1) BYTE DIP SWITCH SETTINGS	-	029
TWO (2) BYTE SOLENOID 10's COUNT	4	869

FIG. 12

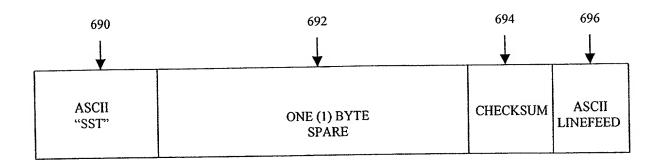


FIG. 13

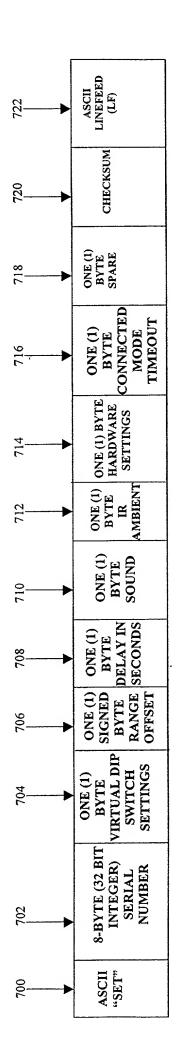


FIG. 14

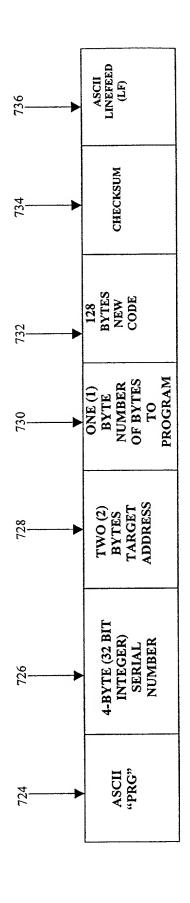
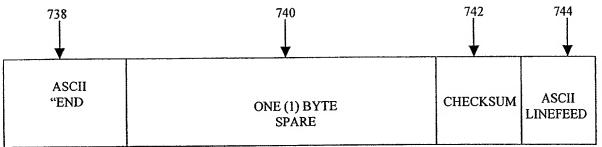


FIG. 15



*

FIG. 16

*

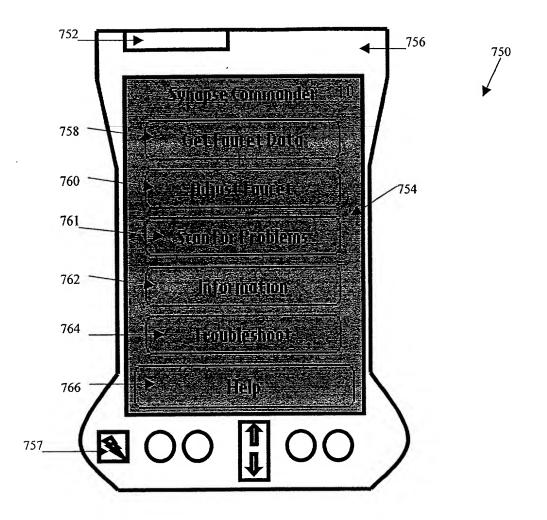


FIG. 17

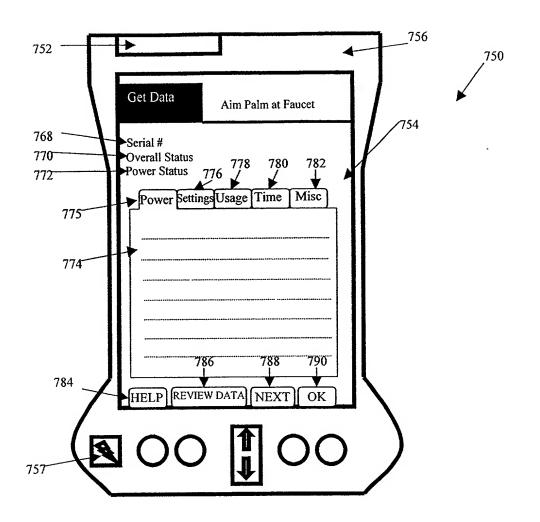


FIG. 18

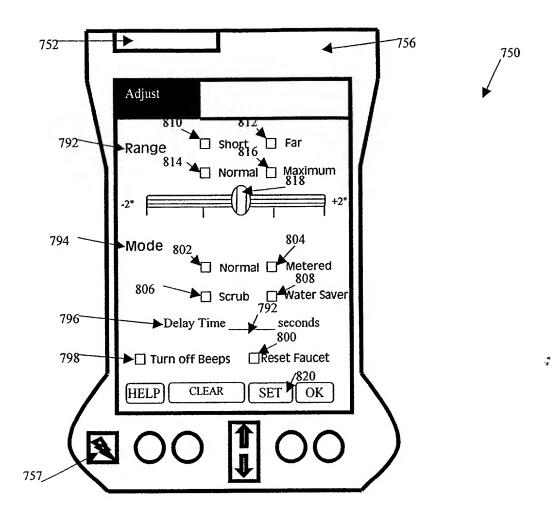


FIG. 19

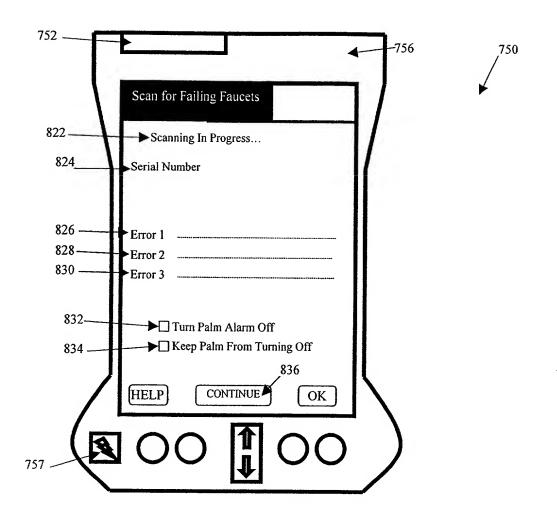
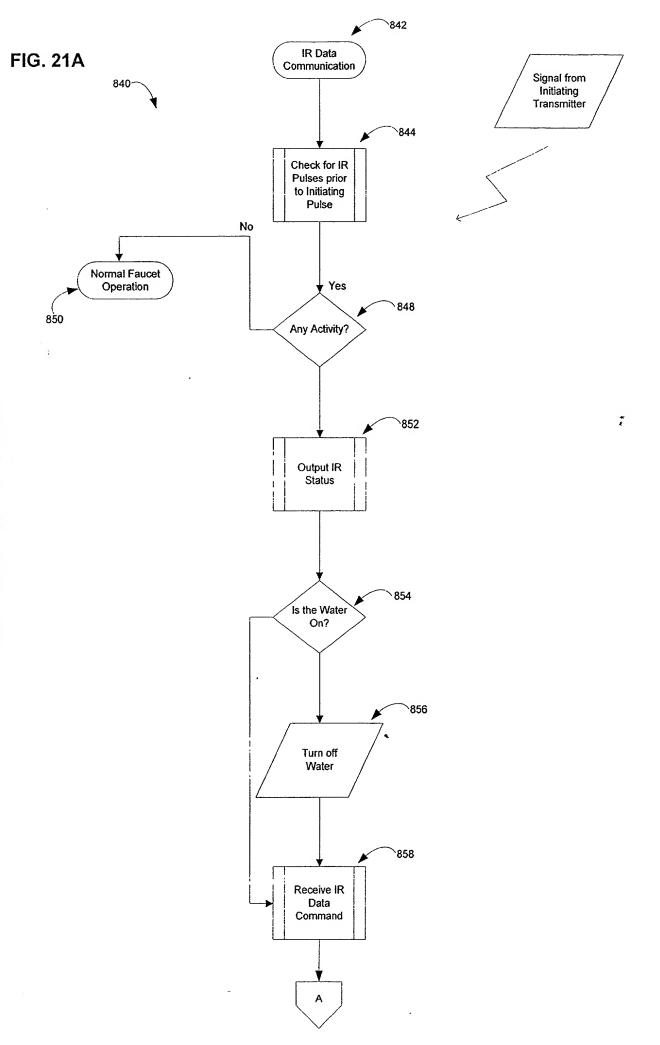
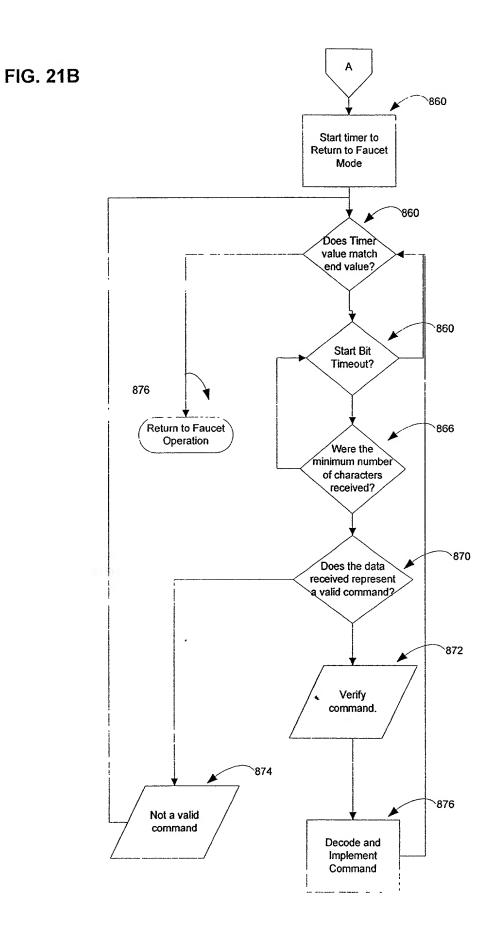


FIG. 20





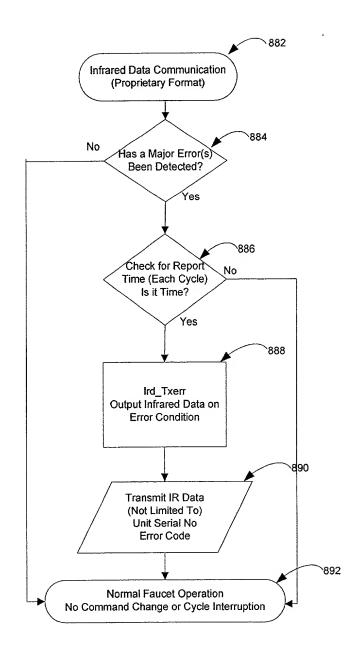


FIG. 23 PRIOR ART

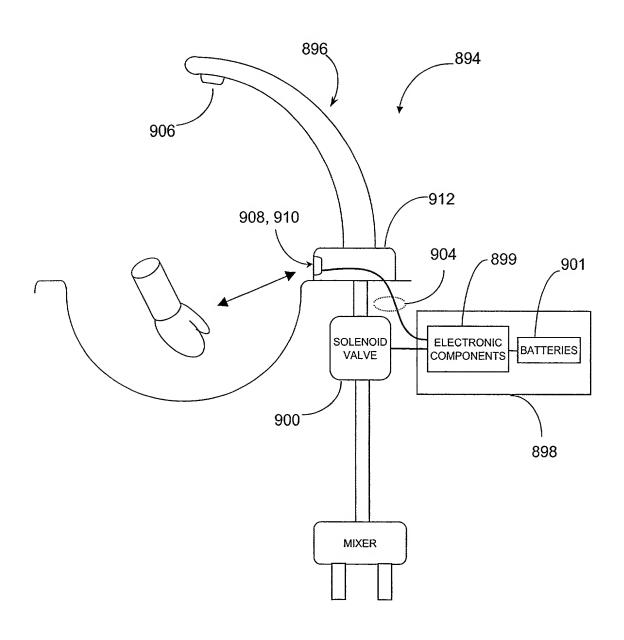


FIG. 24

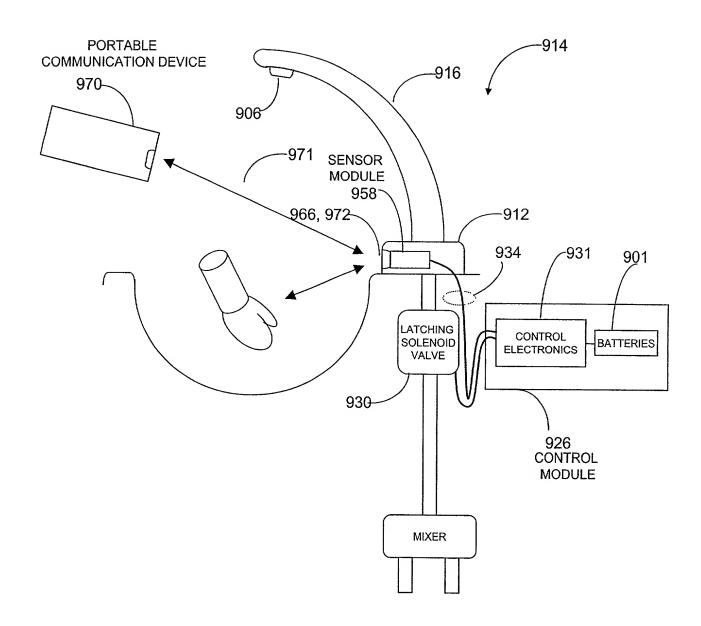
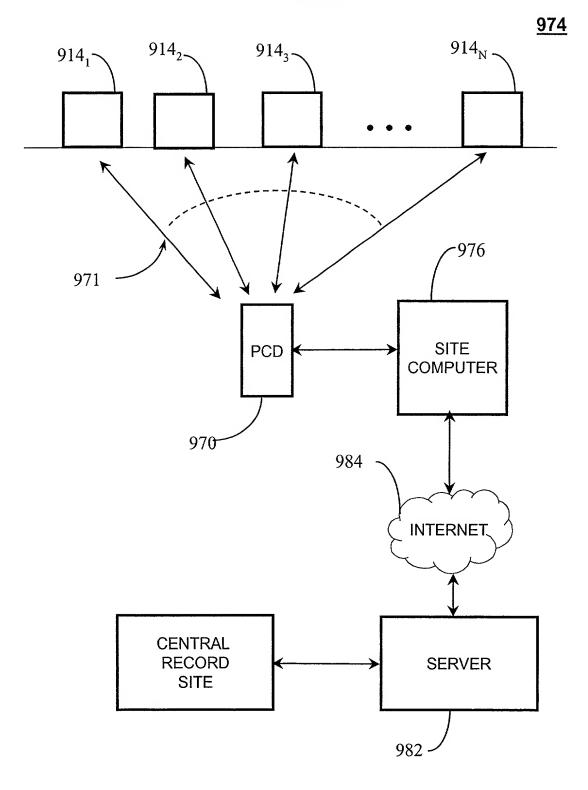


FIG. 25



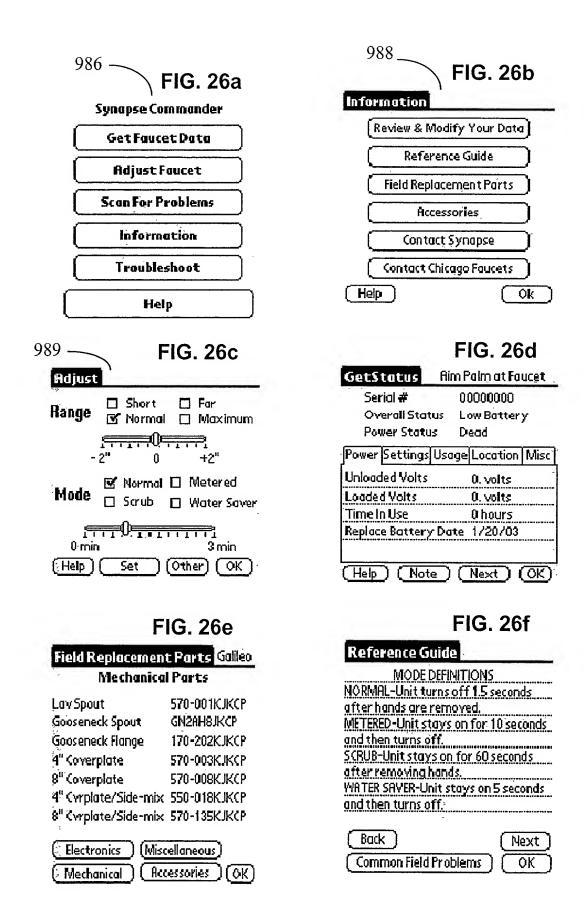
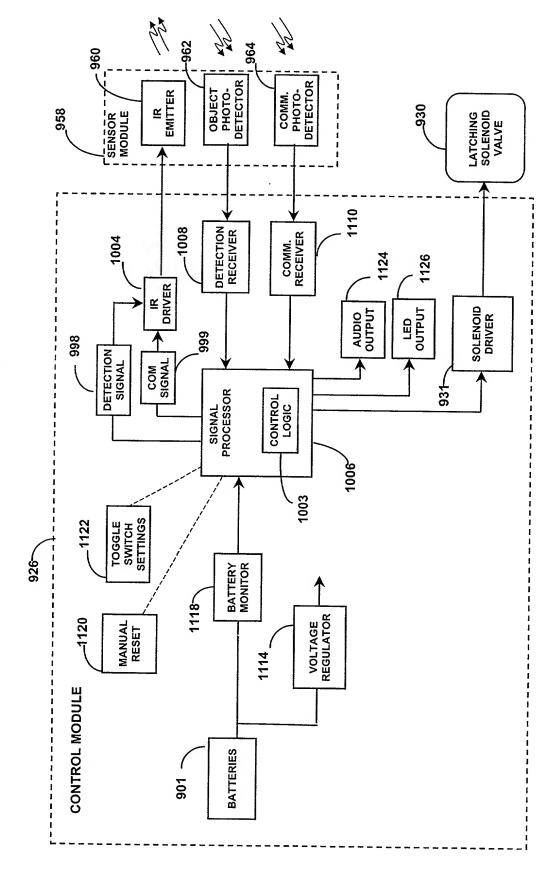


FIG. 27

1



ŧ

